Development of RF Remote Control Based Phase Selector System

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ABSTRACT: An RF remote control based phase selector system was developed. The system is designed with few semiconductors and electromechanical devices. The semiconductor devices includes 555 integrated circuit, 4017 decade counter integrated circuit, transistors and diodes while electromechanical switches used were relays and contactors coupled with other electronic devices. Result shows that, the system starts switching sequentially from one output stage (Phase) to another whenever it senses an RF signal of 300MHz from a VG40T remote control transmitter.

KEYWORDS: Phase Selector, Remote Control, Signal, System, Unit,

I. INTRODUCTION

Today's study shows that, electricity remain an integral part of our modern society [1]. This is because most domestic and industrial activities in our society are dependent of electricity for its operation. However, electricity has also turned out to be life threatening or sometimes lethal when come in direct contact with. For instance, about (28.8%) of home accident cases related to electric shocks due to direct contact with electricity were reported in Iran [4]. Studies also show that the populations at risk are home-based members of the general public including children, students and retired persons [5]. Sometimes, this electric shock occur when these manually operated or switching equipments or appliances such as manual change over switches, phase change over switches, lighting switches and fan switches are faulty (short circuited). Several ways need to be explored so as to avoid direct contact with electricity driven devices especially during system operation. Although, these devices are insulated, the conductors sometimes become exposed due to wear and tear over time. Unknowingly, when touched during operation, an electric shock must be felt and this can lead to severe injuries or fatality. Therefore, there is need for development of remote control devices so as to avoid direct contact with the system.

Furthermore, physically challenge persons (cripple), aged persons and patients sometimes expierience difficulties in operating home switches especially when there is phase failure and under voltage in the mains of the supply. This is because much stress is involved depending on the location of the switch. Nowadays, remote control based appliances and equipments like air conditioners, ceiling fans and lighting systems are developed so as to address these challenges. However, since such concept is applicable to other systems, hence there is need for the development of remote control based phase selector system. Basically, the specific aim of this project is to develop a phase selector system that is operated with the help of RF remote control. The phase selector is designed to have three phase inputs which include red phase, yellow phase and blue phase. However, the system employs VHF radio remote control VG40T and VG40R modules. The VG 40R is a receiver incorporated into the main system so as to communicate with the VG40T transmitter which is the remote controller [9]. The VG40T transmitter has a single push button which is pressed at each interval as it selects the phases. The phase selector system has a convenient operating range of 15 to 20 meters.

Block diagram

II. SYSTEM DESIGN

The block diagram of the RF Control Based Phase Selector consist of RF transmitter, power supply unit, RF receiver unit, Clock unit, decade counter unit, switching unit, three phase unit and single phase load. (See figure 1.0)

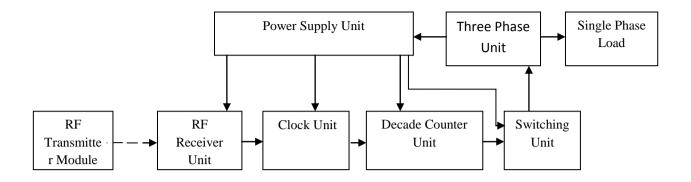


Figure 1.0 Block Diagram of RF Control Based Phase Selector

RF Transmitter Module

This is a Radio frequency remote control transmitter used to communicate with the phase selector. The remote employs VG40 T transmitter module which is powered with a dc voltage (E). It works by pressing the push to-on and off switch (sw_1) so as to transmit 300 MHz pulse signals [9]. (See figure 1.1)

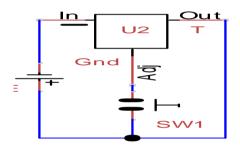


Figure 1.1 Circuit Diagram of RF Transmitter

Power Supply Unit

This unit converts three phase ac voltage into dc voltage. It consist of transformer $(T_1, T_2 \text{ and } T_3)$, diode $(D_1 \text{ to } D_{15})$, fuse $(F_1, F_2 \text{ and } F_3)$ and capacitor $(C_1, C_2 \text{ and } C_3)$. In this case, the transformers step down 220V ac to 12V dc. The 12V dc is converted directly to 12V dc with diodes connected in full wave bridge arrangement. Capacitors are also used as filters to smoothen the pulsating dc voltage from the rectifier output as shown in the circuit in figure 1.2.

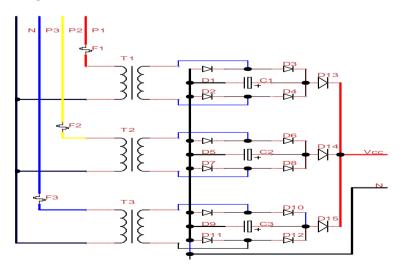


Figure 1.2 Circuit Diagram of Power Supply Unit

RF Receiver Unit

This is the most important unit in this system which acts as a transducer between the transmitter and the main system. In other words, it converts the radio frequency signals into electrical signal. The components used are VG40R receiver sensor module (U_1) , transistor (Q_1) , resistor $(R_g, R_g, and R_{10})$, zener diode (Dz_1) and diode (D_g) . The receiver module requires + 3 volts for operation. Its receiver is designed to receive the 300 MHz signals from the transmitter and is immune to other frequencies [14] [10]. The receiver unit is designed to trigger the base of the transistor with about 300µA causing it to conduct. The zener diode and resistor R_g keeps the input of the receiver module at 3V dc. Also, a diode D_g is employed to keep the signal in forward bias. (See figure 1.3)

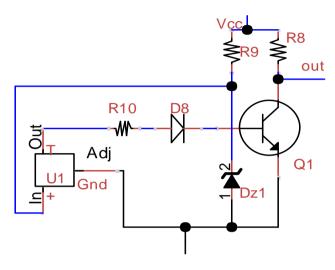
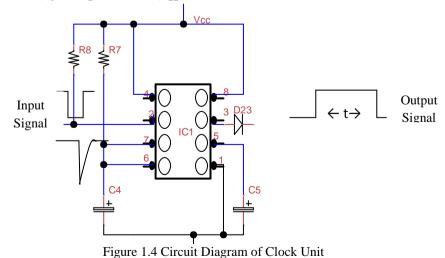


Figure 1.3 Circuit Diagram of RF Receiver Unit

Clock Unit

This is also known as monostable multivibrator unit. It is designed with the popular NE555 timer integrated circuit which is considered as the main brain of this unit. This integrated circuit is configured in a monostable mode as shown in figure 1.4. When the circuit is energized, the output is low and a brief inverted pulse on pin 2 will make the output go high for a period of time determined by the value of R_7 and C_4 . If pin 2 is low for longer than this period, the output will remain high. In other words, it function is to generate a clock pulse of known width (t) from its output when there is a trigger signal from the preceding blocks of the RF receiver unit. The input signal may be a narrow pulse or a spike. The 555 IC is configured with resistor (R_7 and R_8), Capacitor (C_4 and C_5) and diode (D_{22}).



Decade Counter Unit

This is an extremely useful part of the system. The main component used to design this unit is the CD4017 IC chip which is versatile and relatively fit. This integrated circuit has 16 pins and looks like any other 16 pin integrated circuit. Basically, the decade counter has ten outputs which go "high" in sequence when a source of pulses is connected to the clock input and when suitable logic levels are applied to the reset and enable inputs. In this case the CD4017 IC is connected such that it has only 4 outputs. These are A, B, C and D. During operation, each output goes high whenever a clock from the clock unit comes in through pin 14 (See figure 1.5).

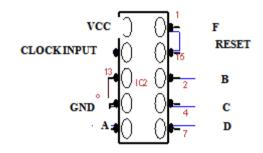


Figure 1.5 Configured Diagram of CD4017 IC Decade Counter

Switching Unit

This unit switches on and switches off electrical contacts. The circuit is built with few semiconductor devices, electromechanical switches and other passive components. This includes, npn transistors (Q_2 to Q_4), relays (R_{sw1} to R_{sw2}), contactors (R_{sw4} to R_{sw6}), diodes (D_{19} to D_{21}), resistors (R_1 to R_6) and light emitting diodes (LED_1 to LED_2).For this unit to switch, each base of the npn transistor receives an input from the decade counter and switch the relay which is used to activate the contactor as described in figure 1.5 below.

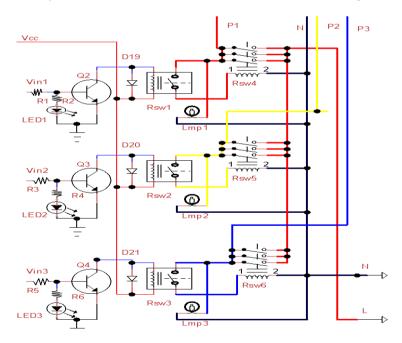


Figure 1.6 Circuit Diagram of Switching Unit

Three Phase Unit

This is the AC mains from public utility. It supplies 220V to 240V AC to the consumer which is tapped and linked directly to all AC loads. This consist of three phases, namely Red phase (P_1), Yellow phase (P_2) and Blue phase (P_2).

General Circuit Diagram

The circuit diagram in figure 1.6 describes the general design of RF remote control based three phase selector.

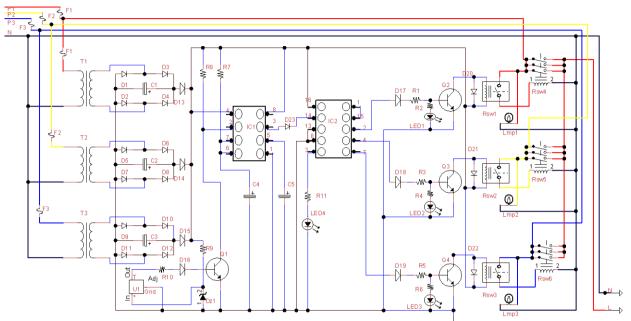


Figure 1.6 Circuit Diagram of RF Remote Control Based Phase Selector

III. CIRCUIT OPERATION

The circuit operation of this system shown in figure 1.6 above is basically a RF remote control based switching system. The system switches from one phase to another when the button (Sw_1) in the remote control transmitter is tapped once. In other words, when the transmitter button is pressed, 315MHz signal is transmitted to the receiver which can only decode such frequency signal. In this case, the RF receiver coupled into the main phase selector system act as a transducer. This converts the radiated wave signal into an electrical signal for circuit operation. However, the base of transistor Q_1 is triggered each time there is signal from the receiver output (T). But, since its collector signal is used as input signal to pin 2 of the NE555 IC, a clock signal is generated across the output at every trigger in the input. This clock signal is basically a square wave signal. At this point, a decade counter start counting each time this clock signal comes in through pin 14. Interestingly, the output signal generated from the decade counter, as a result of the proceeding units is used to switch each phases connected through transistor, relay and contactor. The system also displays it switching state through the present of power indicators. There are; Red (*LED*₁), Yellow (*LED*₂) and blue (*LED*₂) and Blue (*Lmp*₂) serve as input indicators.

MATERIALS AND METHODS

Materials: The materials used in the circuit design are presented in the table in figure 1.0.

SYMBOLS	COMPONENTS	RATING	
D1 -D10	Diodes	1N4001	
D ₁₇ D ₁₉ D ₂₂	Diodes	1N4148	
Dn	Zener Diode	5V	
F1 - F2	Fuses	1A	
Е	Battery	9V	
<i>U</i> ₁	Receiver Module	VG40R	
Uz	Transmitter Module	VG40T	
Sw ₁	Switch	Electronic	
$T_1 - T_2$	Transformers	12V	

<i>C C</i>	Capacitors	3300µF/16V
C1 - C2		
C.	Capacitors	100µF/16V
C _s	Capacitors	100nF
R_1, R_3 and R_5	Resistors	6.8KΩ
R_2, R_4 and R_6	Resistor	220Ω
R ₇	Resistor	56ΚΩ
Rs	Resistor	120Ω
R ₉	Resistor	30Ω
R ₁₀	Resistor	910Ω
R ₁₁	Resistor	330Ω
Q_{z} and Q_{z}	Transistors	2N2222
<i>Q</i> ₁	Transistors	BC548
$LED_1 - LED_2$	Light Emitting Diode; Red, Yellow, Blue and Green Respectively	3.7V
Lmp ₁ -Lmp ₂	Lamps	30W
Rsw ₁ - Rsw ₂	Relay Switches	12V DC
Rsw ₄ - Rsw ₆	Contactors	220V/100A
IC ₁	Integrated Circuit	NE555
IC ₂	Integrated Circuit	CD4017

Table 1.0 List of Circuit Components

IV. METHODS

As far as the design function of this system is concerned, the system was evaluated in two most significant ways. This includes; circuit analysis and testing of system units like the RF receiver unit, the clock unit input and output signals and decade counter unit. Moreover, these units were built and tested sequentially so as to evaluate and obtain the signal required for proper operation of the system. The instruments used during the test were Voltmeter and Multisim simulator. (See figure 1.7 (a) and (b). These instruments were used to obtain and achieve an accurate result for proper system operation.

Circuit Analysis

Power Supply Unit

Peak Inverse Voltage

According to Mehta, the peak inverse voltage is twice the maximum voltage across the half wave [13];

 $P_1V = 2V_m = 2V_s$ Hence PIV = 2 X 12 = 24V

However, the diode used in the rectifier design is 1N4001. This is because it has voltage rating of 50V as required by the circuit.

Average DC Current (*I_c*)

The average DC current was calculated using the expression [11];

$$I_{dc} = \frac{2\pi m}{\pi R}$$

A 200 Ω load resistance was considered across the outputs. Root mean square (R.m.s) of the secondary = 12V

Maximum voltage across the secondary $(V_m) = 12 \text{ x} \sqrt{2} = 17\text{V}$ Decurrent flowing through the load $(I_m) = \frac{2 \times 17}{2} = \frac{34}{2} = 54\text{ m/s}$

Dc current flowing through the load
$$(I_{dc}) = \frac{1}{3.142} \times \frac{1}{200} = \frac{1}{628.4} = 54 \text{mA}$$

Average dc voltage across the load $(V_{dc}) = I_{dc} \times R_L = 54 \text{mA} \times 200\Omega = 10.8 \text{V}$ However, the values of dc voltages (V_{cc}) and currents (I_{dc}) three phases were equally 10.8 Volts and 54 mA respectively.

(1)

(2)

Filter capacitors $(C_1, C_2 \text{ and } C_3)$;

The filter capacitors were calculated using the expression [11];

$$C = \frac{1}{4\sqrt{3}fvR}$$

But for full wave rectifier circuit [3]; f = frequency of ripple voltage = 50Hz y = Ripple factor = 5% = 0.05R = Diode resistance = ?

 $R = \frac{v_{dc}}{t_{dc}} = 120\Omega$ $C = \frac{1}{4\sqrt{3}\times50\times0.05\times120} = C = 481\mu F$

Since 481μ F capacitor is not standard, instead 470μ F was the closest and standard value used as (C_1 , C_2 and C_3).

RF Receiver Unit

Transistor Base resistance (R_{10})

The value of (I_c) and $(I_{\bar{c}})$ were chosen to be 100mA and 5mA respectively according to the BC548 NPN transistor data book [8].

$$R_{10} = \frac{V_{in} - V_{BE}}{I_B}$$
(3)
$$R_{10} = \frac{5 - 0.7}{0.005}$$

 $R_{10} = 860\Omega$

Since the 860 Ω resistor is not a standard value, instead 910 Ω was found to be the closest and standard value for R_{10} .

For **R₈**:

Recall that;
$$R_{c} = \frac{V_{cc}}{I_{c}}$$
 (4)
 $R_{g} = \frac{12}{100 \text{ mA}}$

 $R_8 = 120\Omega$

Maximum Zener Diode Current (*I*_{Zm})

Recall that the VG40R receiver requires 5V across it V_{cc} terminal. To achieve this, 5V 1W zener diode is employed into the design.

But for maximum zener current (I_{Zm}); = $\frac{p_Z}{v_Z}$ (5)

Where; I_{Zm} = Maximum Current of Zener Diode

 P_{ZW} = Power Rating of the Zener diode

 V_z = Voltage Rating of the Zener Diode

$$I_{Zm} = \frac{1W}{5V}$$

$I_{Zm} = 200 \text{mA}$

But the maximum value of resistor connected in series R_9 ;

$$R_{9} = \frac{V_{cc} - V_{z}}{I_{zm}}$$
(6)
$$R_{9} = \frac{(18.1 - 5)V}{200 mA}$$

$R_9 = 29\Omega$

In this case, the value of R_9 was not found to be standard, instead 30 Ω resistor was used.

Zener diode Current (**I**_L)

To calculate the value of current flowing through the zener diode (I_L) at 100 Ω resistance load, we apply;

(7)

(8)

 $I_L = \frac{V_Z}{R_L}$ $I_L = \frac{5V}{100}$ $I_L = 5\text{mA}$

Zener diode Current at Full Load (IZ)

 $I_Z = I_{Zm} - I_L$

 $l_z = (200 - 5) \text{ mA}$

I_Z = 195mA

Clock Unit Output Clock Pulse Width (t) Using the equation for charge capacitor [12], $e = E \left(1 - \varepsilon^{\left(\frac{-r}{Rc}\right)}\right)$ (9) Where; e = Capacitor Voltage (in volt) E = Supply Voltage (in volt) ε = Base for Natural logarithms (approximately 2.718) t = Time taken for Capacitor to charge (in seconds). Recall that in the 555 IC, the capacitor charges until it voltage reaches $(\frac{2}{3})$ +Vcc before it discharges through pin 7. Therefore, the ratio becomes; $\frac{e}{E} = \frac{2}{3}$ Hence, equation (9) becomes $\frac{\epsilon}{\epsilon} = (1 - \epsilon^{(\frac{-t}{RC})})$ (10) $\frac{2}{3} = (1 - \varepsilon^{(\frac{-t}{RC})})$ By simple rearrangement we have; $\frac{2}{3} - 1 = -\varepsilon^{\left(\frac{-t}{RC}\right)}$ $\frac{-1}{2} = -\varepsilon^{\left(\frac{-t}{RC}\right)}$ Multiplying both sides by -1 and taking natural logarithm of both sides, we are arrive at; $\operatorname{In}(\frac{1}{3}) = \frac{-t}{RXC}$ $In(0.333) = \frac{-t}{RXC}$ $-1.0996 = \frac{-t}{RXC}$ Multiplying both sides by -1 we arrive at; $1.0996 = \frac{t}{RXC}$ t = 1.0996 x R x C

t = 1.1 R C(11)Note: $\mathbf{R} = \mathbf{R}_7$ and $\mathbf{C} = \mathbf{C}_4$ But the values of R_7 and C_4 were chosen to be 56K Ω and 100 μ F respectively. Therefore, equation (11) becomes; $t = 1.1 \text{ x } R_7 \text{ x } C_4$ (12) $t = 1.1 \times 1K\Omega \times 1\mu F$ t = 1.1 msTheoretically, the time taken for the capacitor to charge is 1.1 ms.. In other words, this is the output clock signal time (t) and it depends on manual trigger from the proceeding RF unit. Switching Unit Load Resistance $(\mathbf{R}_{\mathbf{L}})$ From Ohm's law expression [7], V = IR(13)By transformation, $V_{cc} = I_c \ge R_L$ (14)Where; Supply Voltage (V_{cc}) = 10.8 V Average dc current (I_{dc}) = 54mA Load Resistance $(\mathbf{R}_{\mathbf{L}}) = ?$ Therefore, $R_L = \frac{v_{cc}}{l_c} = \frac{10.8 v}{54 mA}$ $R_L = 200\Omega$ Hence, the relays whose coil resistance is 200Ω were used in the design.

Base Current (IB)

Using the expression [13];

 $I_c = h_{fe} \times I_B \tag{15}$

Where; $l_c = 54 \text{mA}$

hfe = 35

 $I_B = \frac{I_c}{h_{fe}} = \frac{54mA}{35}$

 $I_B = 1.5 \text{mA}$

Base Resistances $(R_1, R_3 \text{ and } R_5)$

By transforming equation (3) in terms of V_{cc} , V_{BE} I_{B} and R_{B} ;

$$I_{B}R_{B} = V_{CC} - V_{BC}$$
(16)

$$R_{B} = \frac{V_{CC} - V_{BC}}{I_{B}}$$

$$R_{B} = \frac{10.8 - 0.7}{1.5}$$

$$R_{B} = 6.7 \text{K}\Omega$$

However, since 6.7K Ω was not found as a standard value, hence the closest value used in the design for R_1 , R_3 and R_5) was 6.8K Ω .

Current limiting Resistors $(R_2, R_4 \text{ and } R_6)$

Using the relationship to evaluate;

$$R_{x} = \frac{V_{in} - V_{LED}}{I_{LED}}$$
(17)

Where; Supply Voltage (V_{in}) = 10.1V

LED Voltage (V_{LED}) = 3.7V

Maximum Allowable Current $(I_{LED}) = 35$ mA

X = 2, 4, 6

$$R_{x} = \frac{10.1 - 3.7}{35mA} = \frac{6.4}{35mA}$$

$$R_x = 182\Omega$$

Resistor with 182 Ω resistance was not found as standard value, the closest standard value obtained was 200 Ω and it was used as R_2 , R_4 and R_6 .

However, since light emitting diodes are current driven devices, it will proper calculate the current drawn by each LED;

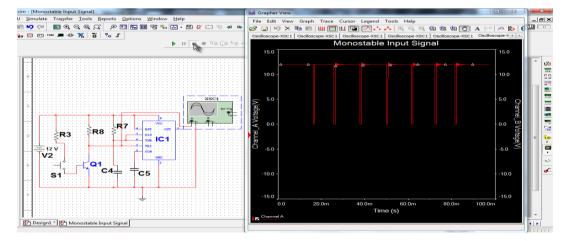
$$I_{LED} = \frac{V_{in}}{R_x}$$
(18)
$$I_{LED} = \frac{10.1V}{200\Omega}$$

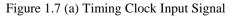
$$I_{LED} = 51 \text{mA}$$

Hence, the amount of current drawn by each LED is 51mA.

V. RESULTS AND DISCUSSION

The following signals in figure 1.7 (a), (b) and figure 1.8 obtained from the oscilloscope describes the nature of the signals at each important unit in the system.





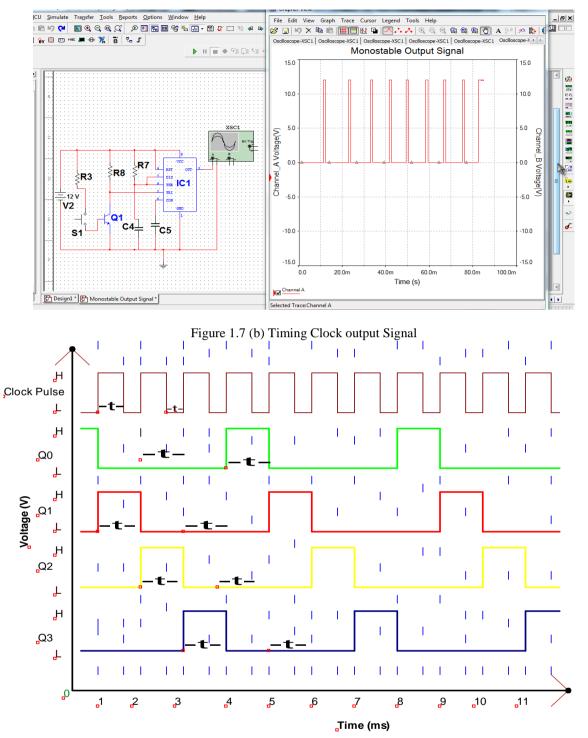


Figure 1.8 Timing diagram of the System

The timing diagram shown in figure 1.8 describes the clock signal, coupled with every output signal from the decade counter. Although the 4017 decade counter has 10 outputs, but in this case only 4 output stages were considered. The stages includes Q_0 , Q_1 , Q_2 and Q_3 . Stage (Q_0) represent the states of the output system (either "ON" or "OFF"), while Q_1 , Q_2 and Q_3 represent the state of the three phases (Red phase, Yellow phase and Blue phase) respectively. The output (Q_0) can only become "HIGH" (H) when there is a clock signal or a shot fired at the decade counter clock input. This output (Q_0) can only remain "HIGH" as far as there is no other clock input. This signifies that all the outputs are shutdown. But whenever another shot is fired or a clock signal comes, the output switches from Q_0 to the next output stage Q_1 . At this point, the output Q_1 becomes "HIGH"

which indicate that only phase one (Red phase) is "ON". But, since the system is configured to operate in a sequential mode, the output Q_2 will also become "HIGH" if another shot is fired from the clock unit. Consequently, output Q_a will continue to remain "LOW" until it receives a clock signal. Assuming the state of the system was at output Q_{a} , and the another clock comes due to the signal from remote control transmitter, it will switch back to output Q_0 since the next output stage is configured so as to reset the system as shown in figure 1.5 and 1.6. Hence, the system will shut down and wait for another clock signal to continue the switching if necessary.

VI. CONCLUSION

It is magnificent to see that the arrangement of active and passive devices in this work has led successfully to the development of RF Remote Control Based Three Phase Selector System. The system is able to change from one phase to another once it received an RF Signal. The RF remote control based phase selector prototype is unique in nature. The system reduces stress, durable, maintenance free, very effective because of moderate switching speed and easy to operate.

Finally, it is recommended that such system should therefore be incorporated into residential buildings and other electrical installation where aged and physical challenge people resites so as to address the immediate problems including electrical hazards associated with switching systems.

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